



CY7C132/CY7C136
CY7C142/CY7C146

2K x 8 Dual-Port Static RAM





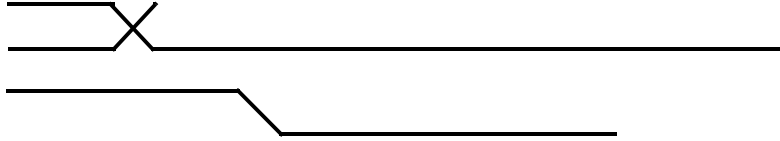
CY7C132/CY7C136
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Switching Waveforms (continued)ed(tPS
Busy Timing Diagram No. 1 (\overline{CE} Arbitration)







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