

**54AC00/54ACT00**  
**Quad 2-Input NAND Gate**  
**General Description**

## Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage ( $V_{CC}$ ) -0.5V to +7.0V

DC Input Diode Current ( $I_{IK}$ )

$V_I = -0.5V$  -20 mA

$V_I = V_{CC} + 0.5V$  +20 mA

DC Input Voltage ( $V_I$ ) -0.5V to  $V_{CC} + 0.5V$

DC Output Diode Current ( $I_{OK}$ )

$V_O = -0.5V$  -20 mA

V

**DC Characteristics for 'AC Family Devices** (Continued)

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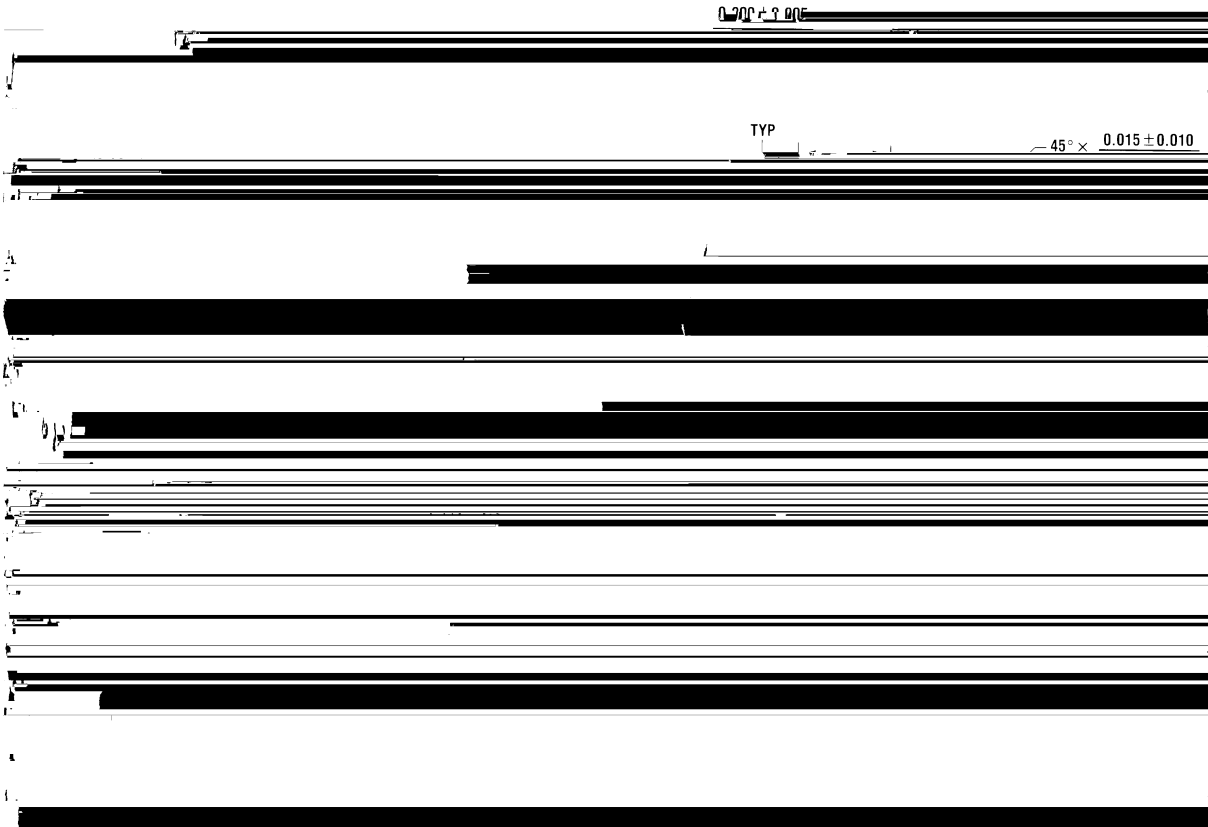
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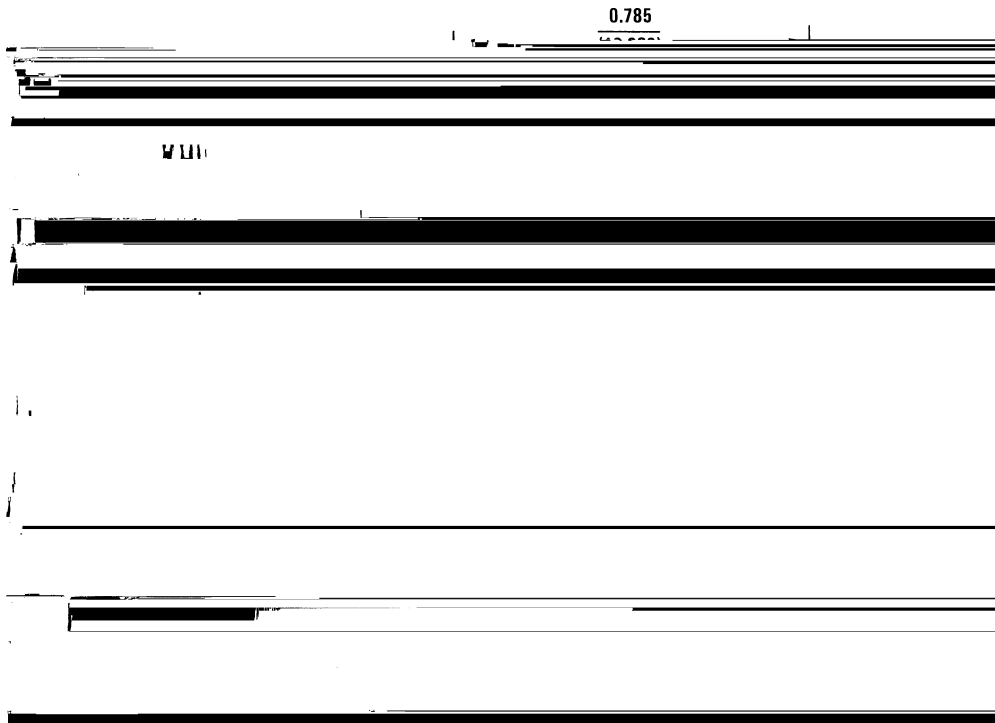
## AC Electrical Characteristics

Symbol	Parameter	V <sub>CC</sub> (V) (Note 9)	54AC	Units	Fig. No.
			T <sub>A</sub> = -55°C to +125°C C <sub>L</sub>		

**Physical Dimensions** inches (millimeters) unless otherwise noted



**20 Terminal Ceramic Leadless Chip Carrier (L)**  
NS Package Number E20A



**14 Lead Ceramic Dual-In-Line Package (D)**  
NS Package Number J14A

