

54ACT823

9-Bit D Flip-Flop

General Description

The ACT823 is a 9-bit buffered register. It features Clock Enable and Clear which are ideal for parity bus interfacing in high performance microprogramming systems. The ACT823 offers noninverting outputs and is fully compatible with

Logic

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required,

AC Electrical Characteristics (Continued)

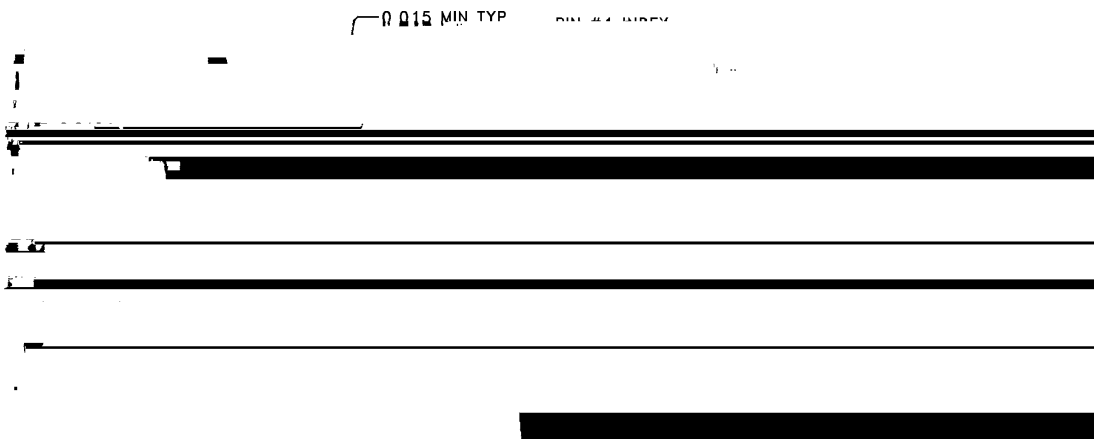
Symbol	Parameter	V _{CC} (V) (Note 4)	T _A = -55°C to +125°C C _L = 50 pF		Units	Fig. No.
			Min	Max		
t _{PLH}	Propagation Delay CP to O _n	5.0	1.0	12.0	ns	
t _{PHL}	Propagation Delay CP to O _n	5.0	1.0	12.0	ns	
t _{PHL}	Propagation Delay $\overline{\text{CLR}}$ to O _n	5.0	1.0	18.0	ns	
t _{PZH}	Output Enable Time $\overline{\text{OE}}$ to O _n	5.0	1.0	11.5	ns	
t _{PZL}	Output Enable Time $\overline{\text{OE}}$ to O _n	5.0	1.0	12.0	ns	
t _{PHZ}	Output Disable Time $\overline{\text{OE}}$ to O _n	5.0	1.0	13.5	ns	
t						

Physical Dimensions inches (millimeters) unless otherwise noted



24 Lead Ceramic Dual-in-line

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



**28 Lead Ceramic Leadless Chip Carrier
Package Number E28A**

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