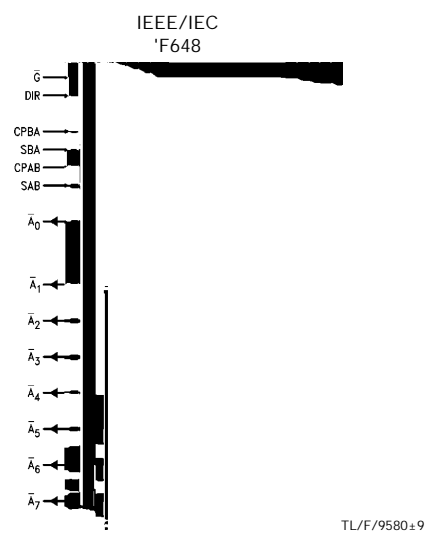


Logic Symbols (Continued)



Connection Diagrams

Pin Assignment
for DIP, SOIC and Flatpak

54F/74F646