
80C51 8-bit microcontroller family
4 K/8 K OTP/ROM low voltage (2.7 V–5.5 V),
low power, high speed (33 MHz), 128/256 B RAM

80C51/87C51/80C52/87C52

BLOCK DIAGRAM

SU00845

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LOGIC SYMBOL

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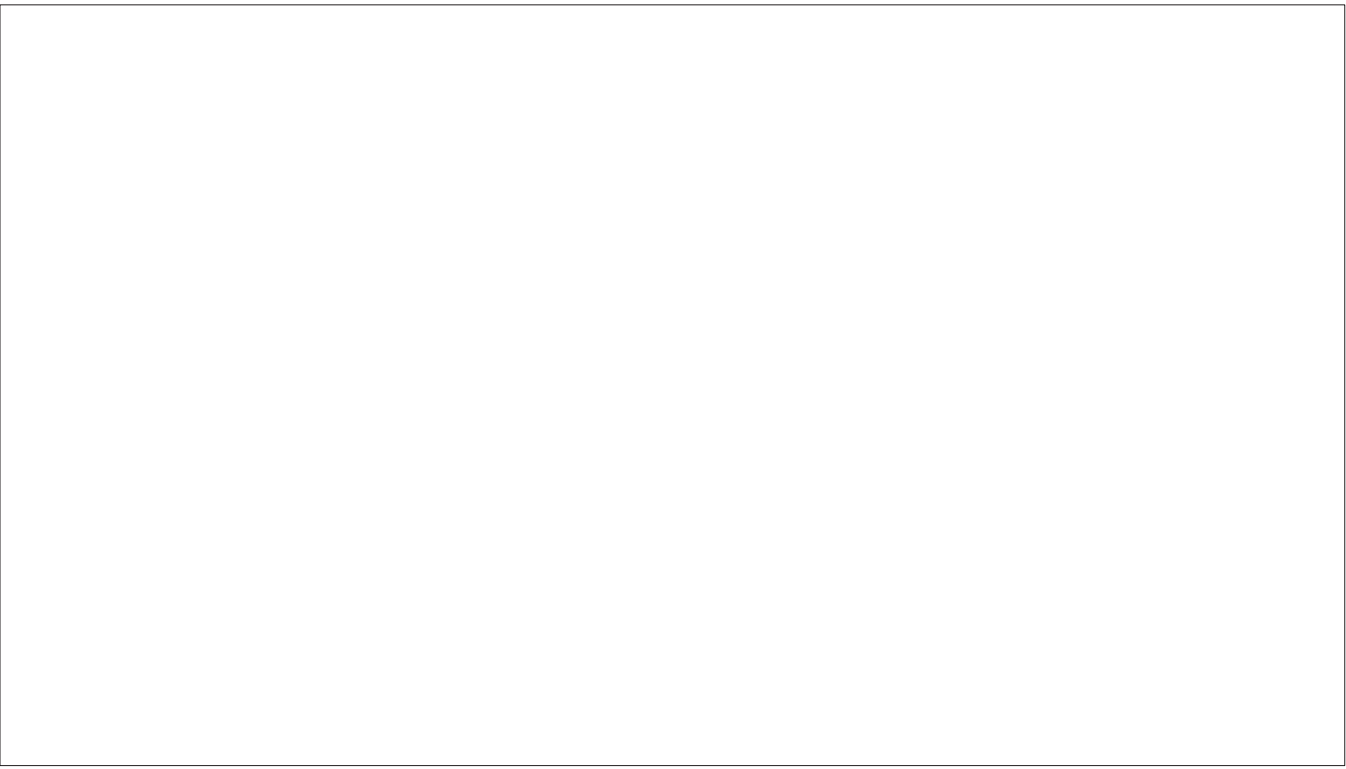
80C51/87C51/80C52/87C52

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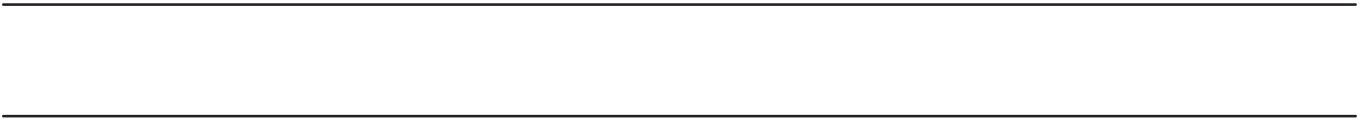
80C51/87C51/80C52/87C52

Baud Rate Generator Mode

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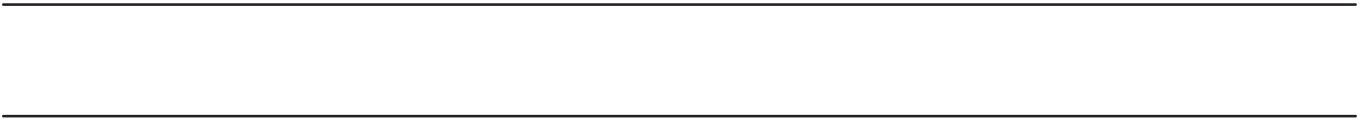
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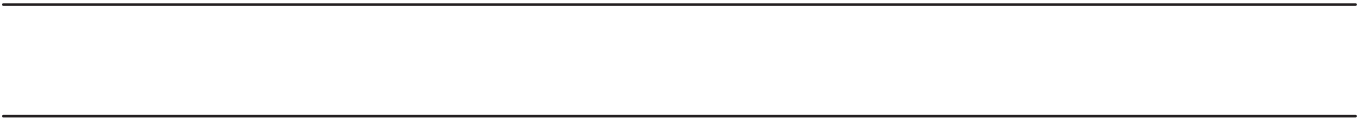
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DC ELECTRICAL CHARACTERISTICS

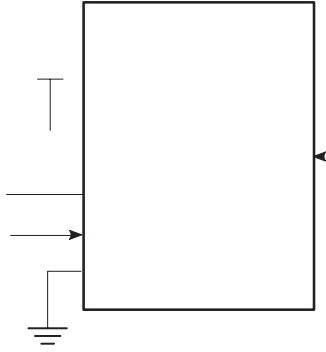
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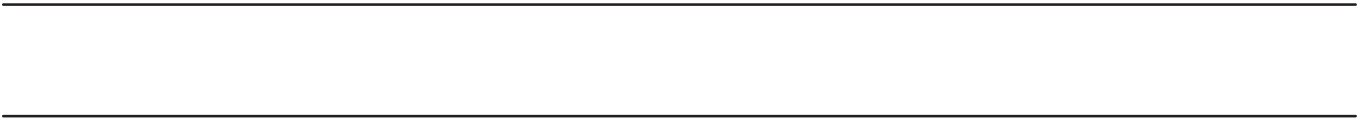


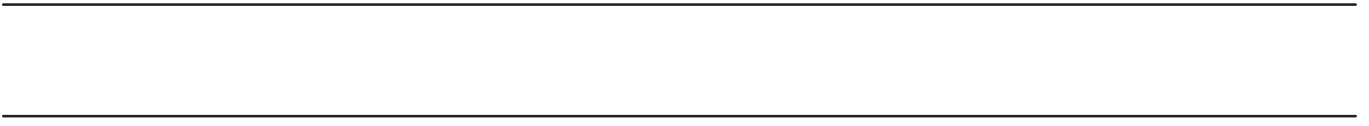


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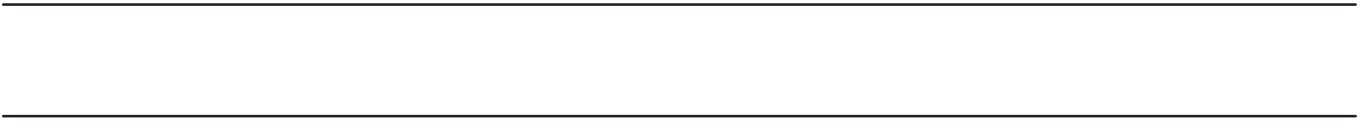
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Io, high sped (33 MHz), 128/256 B RAMy



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Definitions